

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number			
	Filing Date			
	First Named Inventor	Przybylek Piotr		
	Art Unit			
	Examiner Name			
	Attorney Docket Number	LHUD-02501-NUS		

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
/KE/	1	5651128		07/22/1997	Gaultier, Jean-Marie	

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/KE/	1	0991081	EP		04/05/2000	Lardeau, Celine		<input type="checkbox"/>

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NON-PATENT LITERATURE DOCUMENTS

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/KE/	1	BAHOUT Y: "Combined FLASH and EEPROM integrated circuit", ELECTRONIK INDUSTRIE, XX, XX, vol.28, no. 10, 1997, pages 48, 50-51	<input type="checkbox"/>
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Examiner Signature	/Kevin Ellis/	Date Considered	02/14/2008
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